

WHAT IS CLAIMED IS:

1. A circuit simulation method for carrying out operational simulation of an electronic circuit through a network, said circuit simulation method comprising the steps of:

sending, from a first information processing apparatus connected with said network, circuit data indicating characteristics of said electronic circuit through said network;

receiving said circuit data at a second information processing apparatus connected with said network, said second information processing apparatus being so arranged that circuit models describing electronic circuit operations are stored to enable operational simulation thereon;

carrying out operational simulation of said electronic circuit at said second information processing apparatus using said circuit models and said circuit data; and

sending results of said operational simulation from said second information processing apparatus through said network.

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2. A circuit simulation method as claimed in claim 1,

wherein, at said step of sending results of said operational simulation, said results of said operational simulation are sent to said first information processing apparatus from said second information processing apparatus through said network.

3. A circuit simulation method as claimed in claim 1,

wherein each of said circuit models includes device models, each indicating operations of a device constituting a circuit.

4. A circuit simulation method as claimed in claim 3,

wherein each of said device models includes transistor models, each indicating operations of a transistor constituting said circuit.

5. A circuit simulation apparatus for carrying out operational simulation of an electronic circuit through a network, said circuit simulation apparatus comprising:

means for receiving, from a first information processing apparatus connected with said network, circuit

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data indicating characteristics of said electronic circuit through said network;

means for storing circuit models describing electronic circuit operations;

means for carrying out operational simulation of said electronic circuit using said circuit models and said circuit data; and

means for sending results of said operational simulation through said network.

6. A circuit simulation apparatus as claimed in claim 5,

wherein said means for sending results of said operational simulation sends said results of said operational simulation to said first information processing apparatus through said network.

7. A circuit simulation apparatus as claimed in claim 5,

wherein each of said circuit models includes device models, each indicating operations of a device constituting a circuit.

8. A circuit simulation apparatus as claimed in claim 7,

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wherein each of said device models includes transistor models, each indicating operations of a transistor constituting said circuit.

9. A computer program product storable on a storage medium for carrying out operational simulation of an electronic circuit through a network in a circuit simulation system where said computer program product is run on a computer in which circuit models describing electronic circuit operations are stored, said computer program product comprising the steps of:

receiving, from a first information processing apparatus connected with said network, circuit data indicating characteristics of an electronic circuit through said network;

carrying out operational simulation of said electronic circuit using said circuit models and said circuit data; and

sending results of said operational simulation through said network.

10. A computer program product as claimed in claim 9,

wherein, at said step of sending results of said operational simulation, said results of said operational

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simulation are sent to said first information processing apparatus from said second information processing apparatus through said network.

11. A computer program product as claimed in claim 9,

wherein each of said circuit models includes device models, each indicating operations of a device constituting a circuit.

12. A computer program product as claimed in claim 11,

wherein each of said device models includes transistor models, each indicating operations of a transistor constituting said circuit.